

Low Voltage High Gain Amplifier Circuits

ABSTRACT

In one embodiment, the present invention provides an amplifier circuit including a cascode input stage coupled to a differential stage. In another embodiment, an amplifier includes a differential stage coupled to a common gate stage. Embodiments of the present invention also include an improved low voltage amplifier using a 3-stage topology including a wide-swing folded-cascode input stage followed by a differential gain stage which provides improved gain and output compliance. Embodiments of the present invention improve the available cascode stage headroom and reject process and temperature induced parametric variations.

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